

FIG. 1

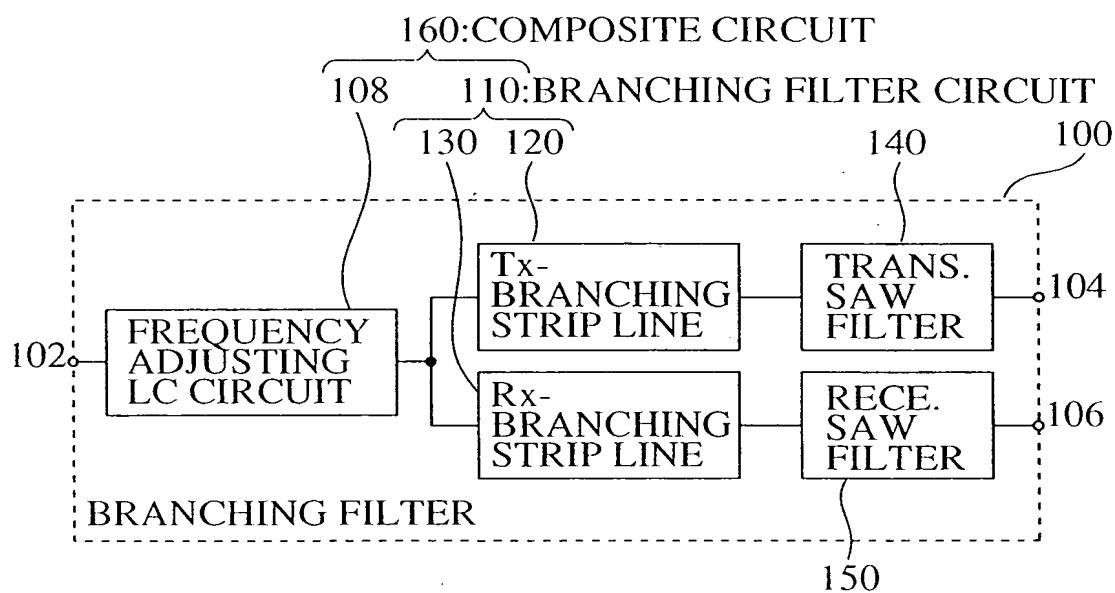


FIG. 2

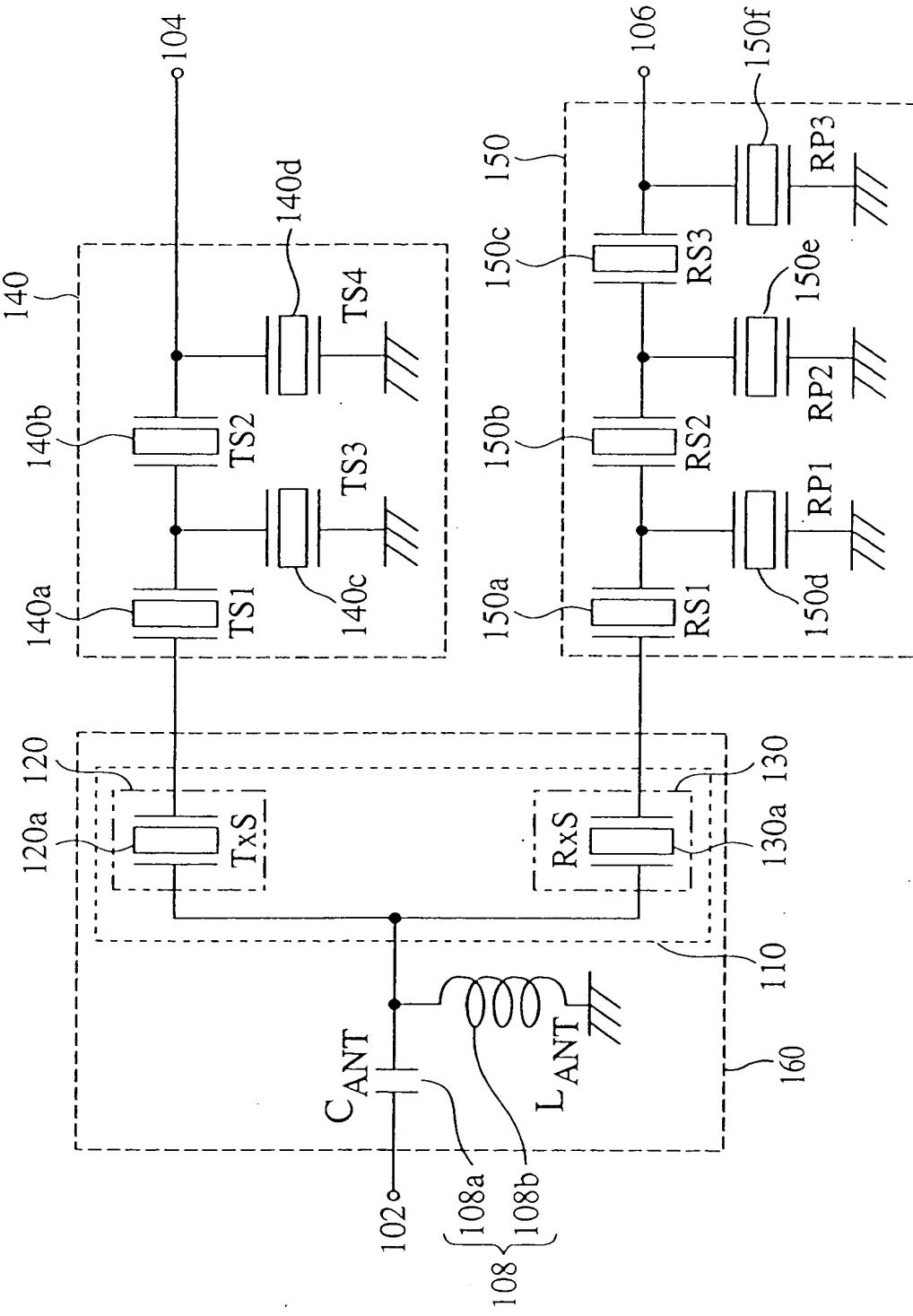


FIG.3

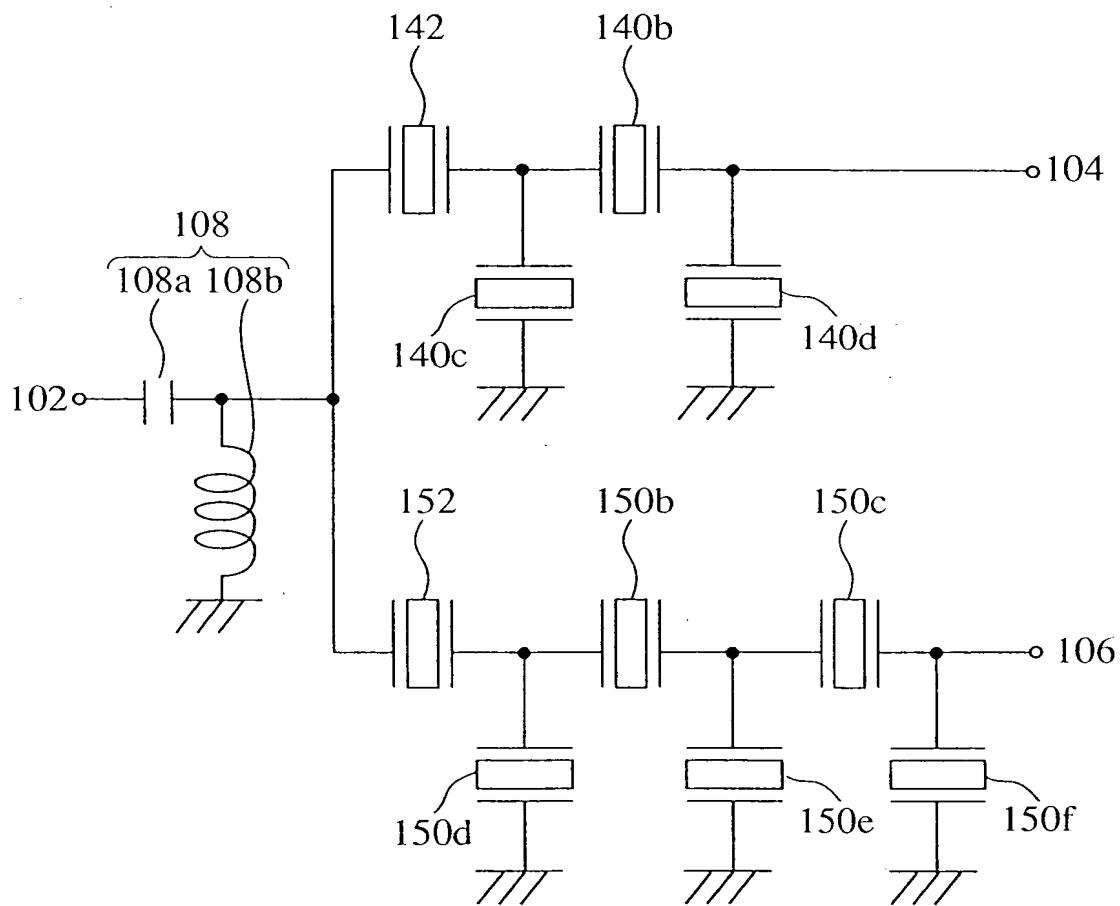


FIG. 4(A)

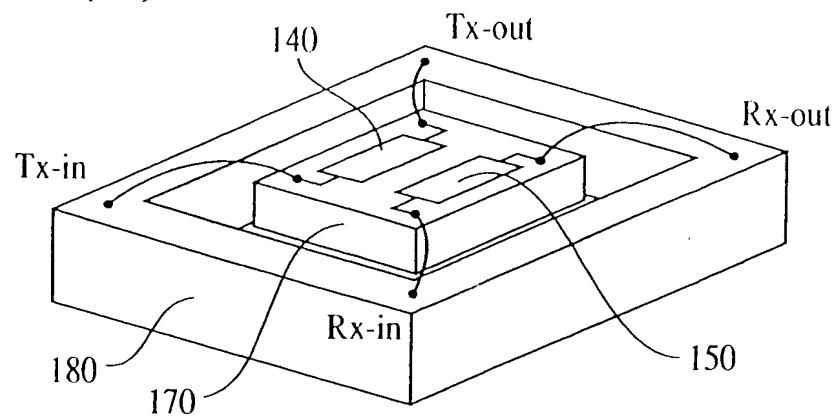


FIG. 4(B)

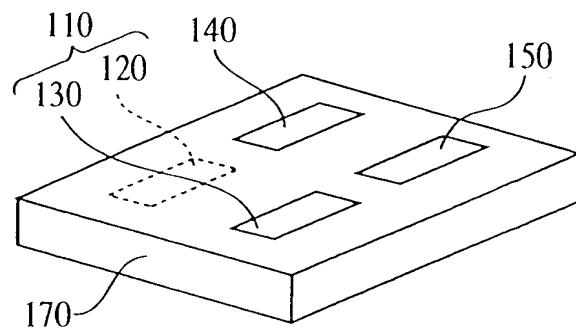


FIG. 4(C)

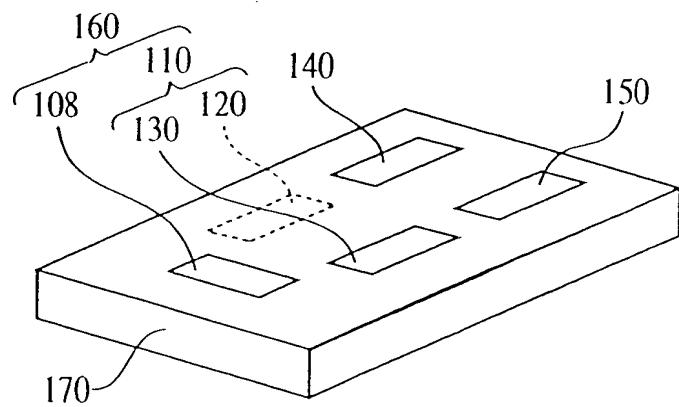


FIG.5

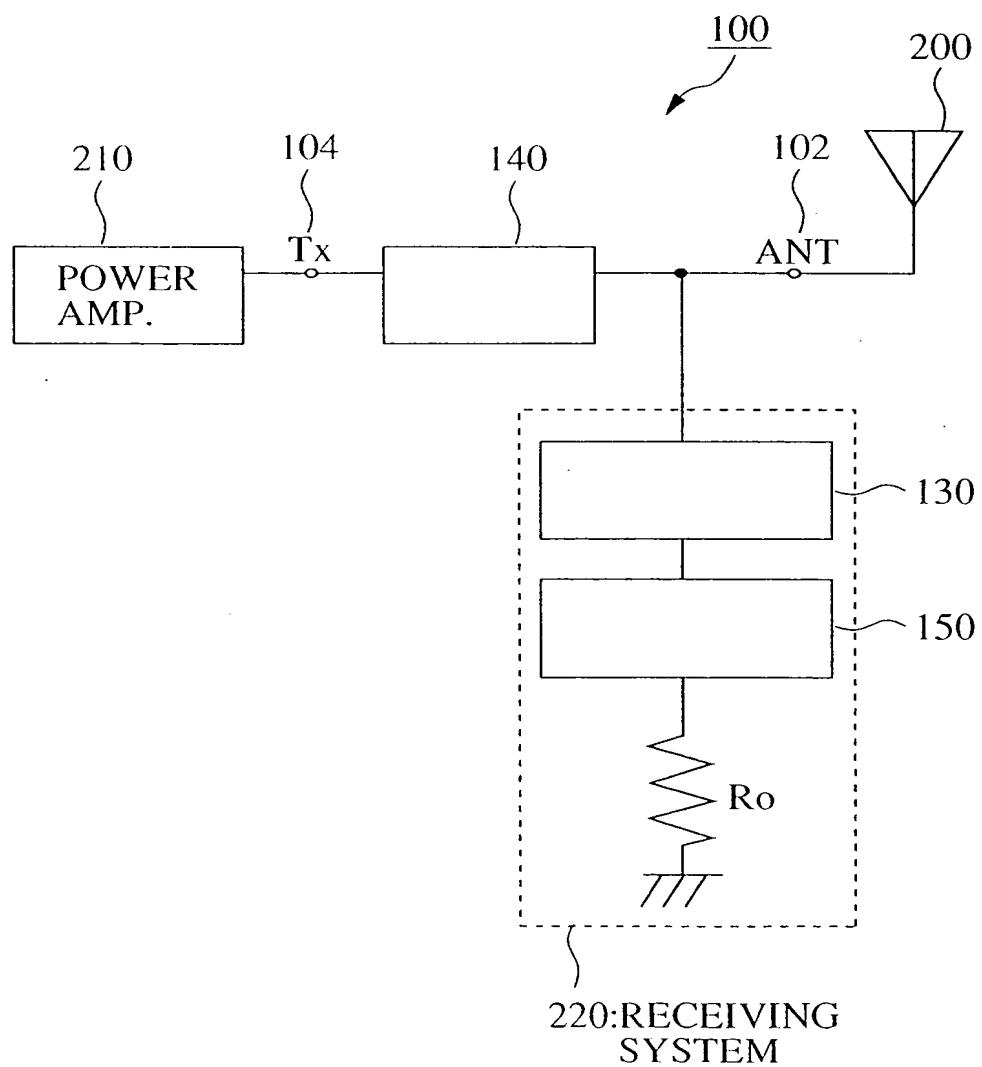


FIG. 6

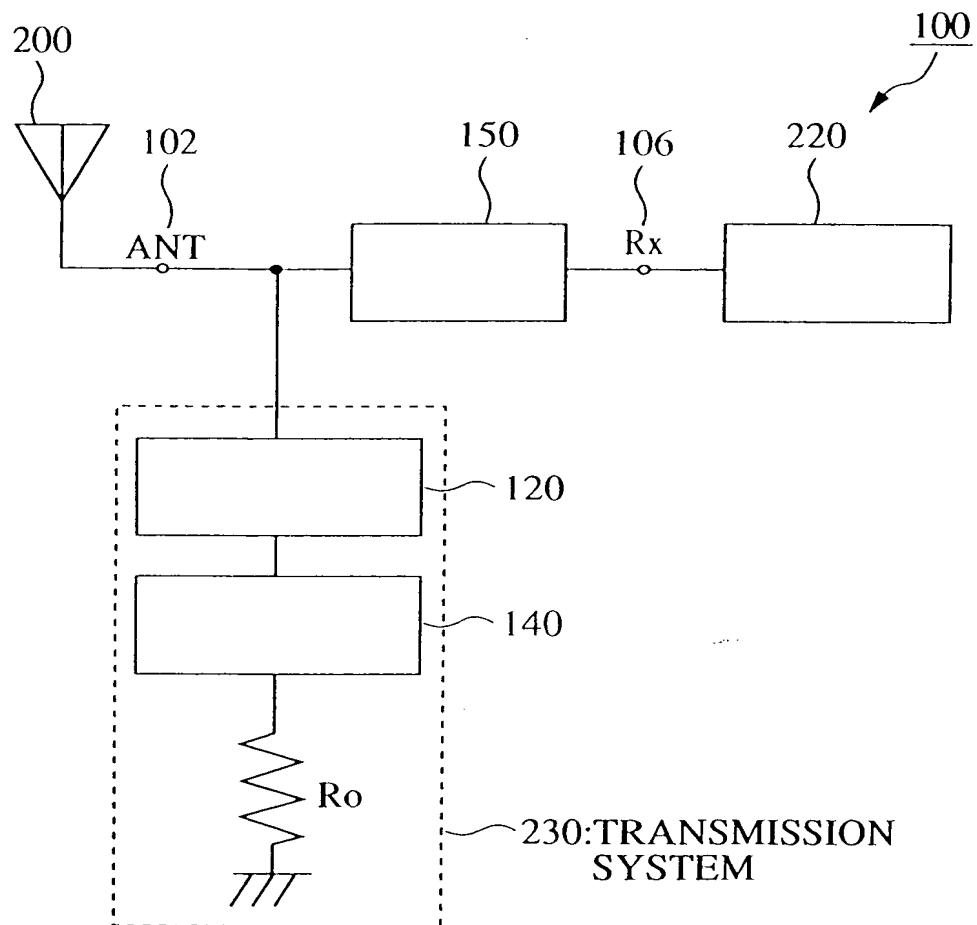
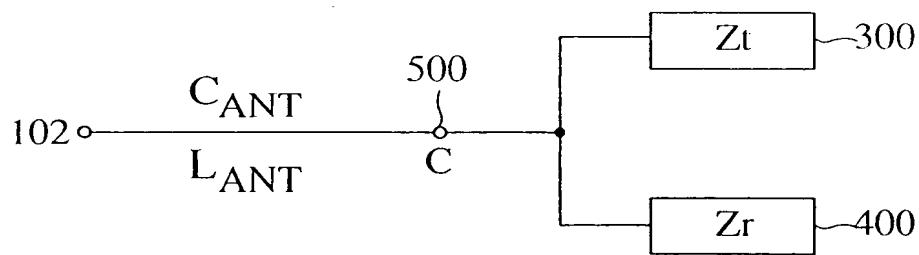


FIG.7



DEPARTMENT OF ELECTRONICS

FIG.8(A)

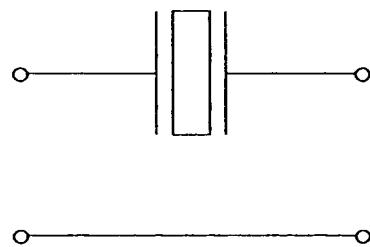


FIG.8(B)

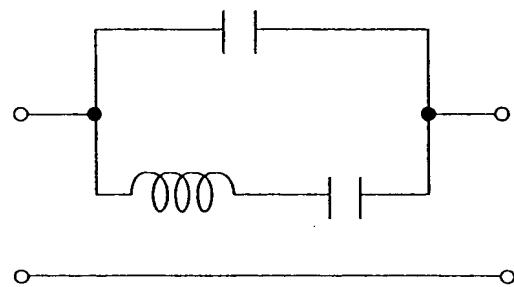
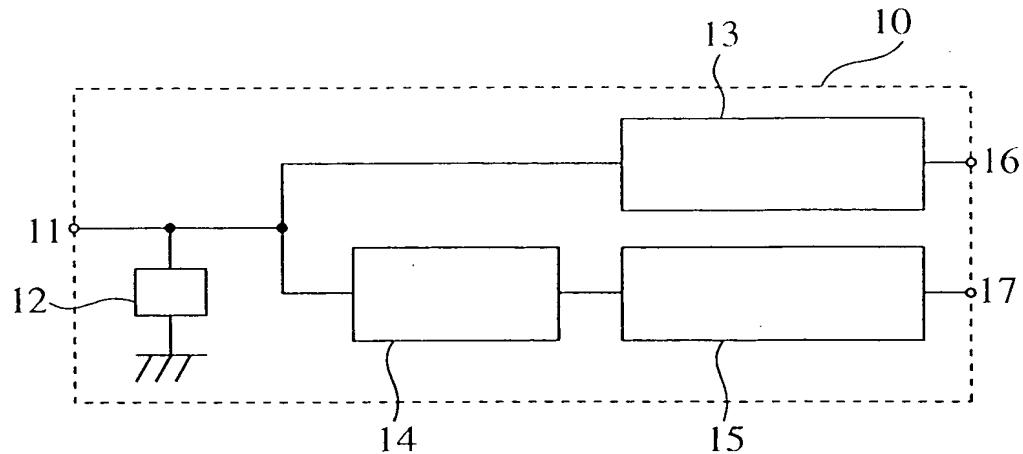
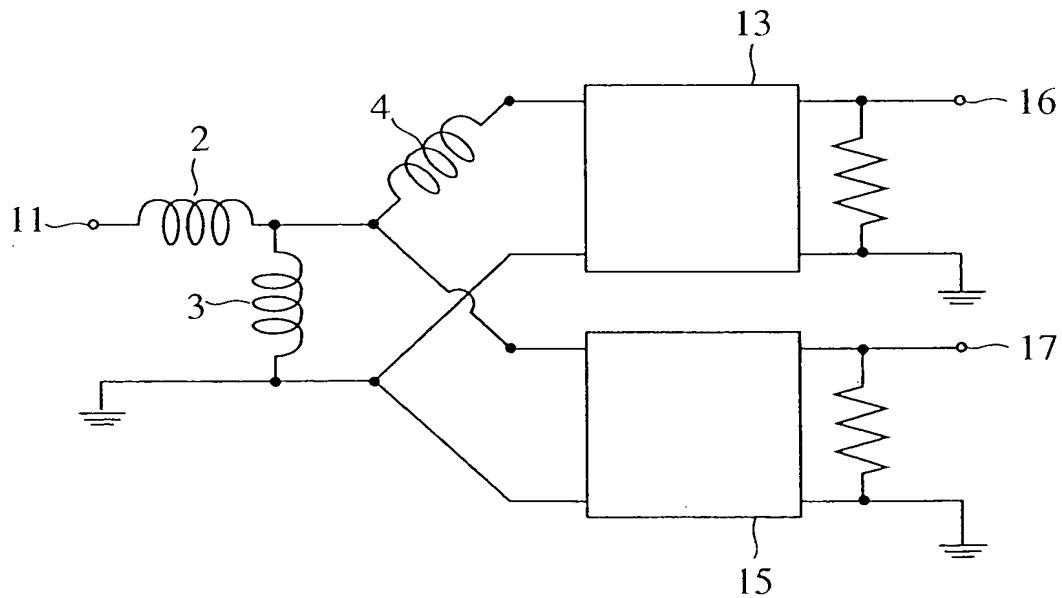


FIG. 9



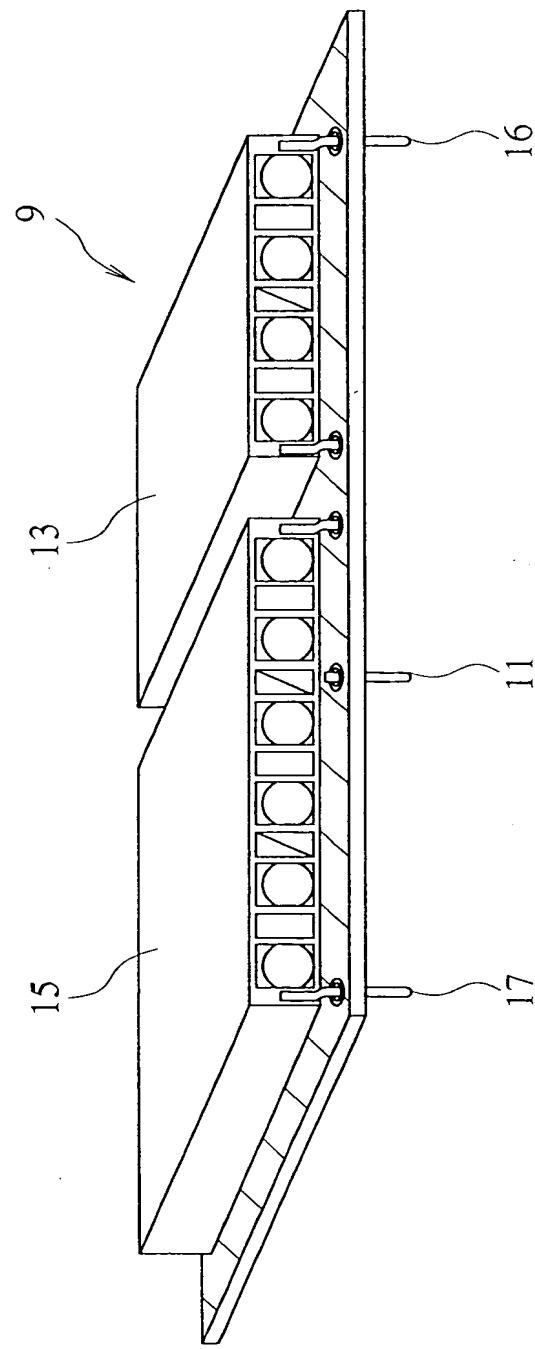
PRIOR ART

FIG. 10



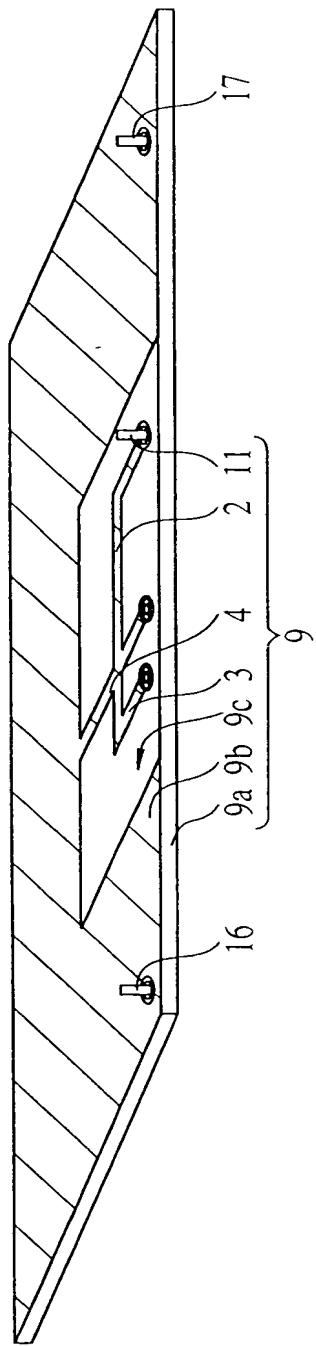
PRIOR ART

FIG. 11



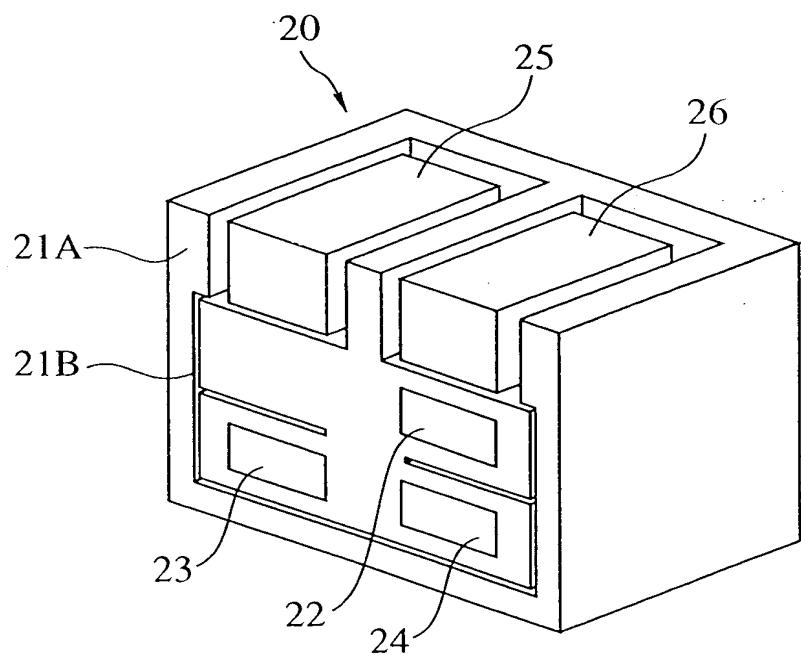
PRIOR ART

FIG. 12



PRIOR ART

FIG. 13



PRIOR ART